Characterization of stain etched p-type silicon in aqueous HF solutions containing HNO$_3$ or KMnO$_4$

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1. Introduction

Intense scientific and technological interests in porous silicon focus on the remarkable high surface area, enhanced chemical reactivity and novel physical properties of this material. Biological [1–3] and chemical [4–7] sensors have been prepared on chemically modified porous silicon thin films. Porous silicon can also be used in optoelectronics [8], photovoltaics [9], micromachining [10,11] and in fabrication of diffusion membranes [12]. Porous silicon is typically formed either by anodic etching in HF or by stain etching in HF/oxidizing agent [13]. The structures, and thus the properties of the porous silicon films prepared by these methods, depend on the silicon type and the etching conditions used, such as the chemical composition of the etchant and the etching time [14–17]. In stain etching no external bias is required and holes necessary for sustaining the oxidative dissolution of silicon are supplied by HNO$_3$ or other strong oxidants. Porous layers formed by stain etching both on single and polycrystalline Si posses similar structural and light-emitting properties to those obtained by electrochemical etching [18,19]. The homogeneous Si surface can be stain etched rather uniformly, and the resulting porous silicon has been used as an anti-reflection coating on silicon-based solar cells [20]. Various oxidizers added to HF solutions have been tested for stain etching, examples being HNO$_3$, NaNO$_2$, CrO$_3$ [21] and Fe(III), Mn(VII) [22]. Anisotropic textured silicon was obtained by stain etching of Si in aqueous HF/HNO$_3$ solutions at low etching rates [23].

AC electrical impedance characterization of porous silicon allows to obtain good information about the electrical conductivity mechanisms, stability of materials towards ambient conditions, and the morphology of porous film through theoretical modeling of the impedance spectra [24]. Certain studies have applied this technique to porous silicon as a substrate for polymers [25], the contact between Au and porous silicon [26], and the use of porous silicon as a guest matrix for metallic cations or for metallic oxides [27].

Stain etching of Si in HF solutions containing oxidizing agents at open-circuit potential is of interest because many industrial etching processes take place without potential control. The aim of the present work is to study the electrochemical impedance behavior of p-Si surface stain etched in aqueous HF solutions containing HNO$_3$ or KMnO$_4$ (as oxidizing agent) as a function of time and concentrations of HF and oxidants.

2. Experimental

Silicon samples were prepared from boron-doped p-type (0.002–0.004 Ohm cm) single crystal wafer of (100) orientation manufactured by Shin-Etsu Co. (Tokyo, Japan). The Si samples were cut and pressed against an O-ring sealed in the wall of an electrochemical cell, leaving 0.6 cm$^2$ exposed to the electrolyte. The back contact was made via In–Ga alloy. Finally, a copper wire was connected to the In–Ga at the surface by silver paste. The electrochemical cell used was a cylindrical polytetrafluoroethylene cell 9 cm high and 6 cm in diameter; The counter electrode was a Pt sheet of 1 cm$^2$ area. Before the etching process each sample was cleaned with acetone for 10 min then etched in 7.5 M HF solution for few minutes to remove organic impurities and the native oxide layer from the wafer. The etching solutions were prepared from analytical grade chemical reagents in deionized water. The
oxidizing agents used in the present work were nitric acid, HNO$_3$, and potassium permanganate, KMnO$_4$.

The electrochemical impedance spectroscopic investigation of p-Si was recorded during the etching process in aqueous HF-oxidizing agent solutions at the open-circuit potential using the IM6d–AMOS system (Zahner Electrik GmbH & Co., Kronach, Germany). The input signal was 10 mV peak to peak in the frequency domain 0.1–10$^5$ Hz. Each cycle of impedance measurements lasted about 338 s and was carried out at a constant room temperature of 27 ± 1$^\circ$C. The morphology of the etched Si surface was examined by scanning electron microscopy (Philips XL-30). The chemical analysis of the silicon surface after etching was performed by energy dispersive X-ray (EDX).

3. Results and discussion

3.1. Effect of etching time

To study the effect of etching time on the electrochemical characteristics of the surface of p-Si in aqueous HF-oxidizing agent solutions the impedance measurements were carried out after different etching times, i.e. 1, 60, 120 and 180 min. The oxidizing agents used were HNO$_3$ and KMnO$_4$. The concentration of KMnO$_4$ in these measurements was kept constant at 0.2 M and the concentration of HF was 11.0 M. A concentration of 0.2 M HNO$_3$ had a negligible etching effect, so an etchant solution of 7.0 M HF and 2.0 M HNO$_3$ was used to produce the porous structure.

3.1.1. Effect of etching time on the p-Si in aqueous HF/HNO$_3$ solution

The impedance response (in the Nyquist format) of p-Si in aqueous 7.0 M HF/2.0 M HNO$_3$ solution exhibits a capacitive loop at high frequencies and a small inductive loop at low frequencies. This inductive loop appears clearly after long etching times. Previously, inductive loops have also been found during silicon etching in HF solutions near the open-circuit potential [28,29]. From Fig. 1 it can be observed that the impedance value decreases with increasing the etching time, implying an increase of silicon dissolution with time. A similar behavior in the impedance of the n-Si has been observed upon addition of Na$_2$CrO$_4$ to the etchant HF solution [29]. This observation is also in line with previous impedance data in that electrical impedance of porous silicon is much lower than that of bare Si [30]. The capacitive loop can be attributed to the space charge capacitance parallel with the surface states lying in the forbidden energy gap, while the inductive loop appears related to an active surface growth due to the formation of porous silicon [31]. Fig. 2a shows the scanning electron photograph of the silicon surface after etching in an aqueous solution of 7.0 M HF/2.0 M HNO$_3$ for 180 min. From the figure it is clear that the surface of the etched silicon becomes porous. Therefore, the SEM is in good agreement with the impedance results.
Fig. 2. (a) The scanning electron photograph and (b) the EDX analysis of p-Si surface after etching in 7.0 M HF containing 2.0 M HNO₃ for 180 min.

Fig. 3. Nyquist impedance plots of p-Si after etching in 11.0 M HF containing 0.2 M KMnO₄: (a) for 1 min, (b) for 60 min, (c) for 120 min, (d) for 180 min.
Fig. 4. Bode impedance plots of p-Si after etching in 11.0 M HF containing 0.2 M KMnO₄: (a) for 1 min, (b) for 60 min, (c) for 120 min, (d) for 180 min.

3.1.2. Effect of etching time on the p-Si in aqueous HF/KMnO₄ solution

Fig. 3 shows the impedance measurements for p-Si in aqueous 11.0 M HF/0.2 M KMnO₄ solution at different etching times. Fig. 3 represents Nyquist impedance plots which show that the impedance spectrum exhibits one capacitive loop after 1 min. At high frequencies the impedance response is dominated by the space charge capacitance of the Si/electrolyte interface, and at middle frequencies the value of the capacitance can be attributed to the presence of the surface states. Such surface states may result from the modifications of the surface properties during the dissolution reaction of silicon [29]. As the etching process is going on, the high frequency part of the impedance is enhanced with time and an additional high frequency capacitive loop in the impedance plot is recorded. The size of this loop increases with increasing the etching time (Fig. 3). The impedance behavior, in this case, can be explained by dissolution of Si at the early stages of the etching process to form pores onto the silicon surface, thereafter a passive layer starts to deposit on the surface. Bode plots of p-Si in the same etchant solution in Fig. 4 show the presence of two peaks for the phase at the selected frequency range which confirm occurrence of two processes at the p-Si/solution interface as indicated by Nyquist plots.

The surface was also investigated by SEM and EDX. Fig. 5a shows the scanning electron photograph of p-Si etched in 11.0 M HF/0.2 M KMnO₄/H₂O solution. From this figure it is clear that the silicon surface is covered with a film (stain film). The EDX spectrum of the etched sample (Fig. 5b) indicates that the elements present on the etched surface are mainly Si, K and F. The presence of the observed amounts of F and K on the surface is attributed to depo-
position of a layer of K$_2$SiF$_6$ on the silicon surface during the etching process. This layer was previously observed by X-ray diffraction on the p-type silicon during etching in 12.0 M HF/0.15 M KMnO$_4$/H$_2$O solution [32]. Therefore, the results drawn from the presented impedance behavior of the Si surface etched in 11.0 M HF/0.2 M KMnO$_4$/H$_2$O solution agree well with those of SEM and EDX measurements.

3.2. Effect of oxidizing agent concentration

3.2.1. Effect of HNO$_3$ concentration

p-type silicon was etched in 3.0 M HF solutions containing different HNO$_3$ concentrations, i.e. 0.3, 0.5, 1.0 and 2.0 M. The impedance diagrams of the etched surfaces in the different solutions are shown in Fig. 6. The diagrams reveal one capacitive loop. To clarify the relationship between the impedance responses of the etched Si surface and its morphology, the impedance data have been analyzed using an equivalent circuit consisting of a minimum number of elements which fit the whole set of experimental data with fair precision (i.e. the discrepancy between experimental data
Fig. 8. Nyquist impedance plots of p-Si after immersion in 3.0 M HF containing different concentrations of KMnO₄: (a) 0.1 M KMnO₄, (b) 0.2 M KMnO₄, (c) 0.3 M KMnO₄.

and theoretical fit is about 1.3%). As a result of the simulation we have obtained the equivalent circuit model shown in Fig. 7 which is similar to that obtained in a previous work [30]. The equivalent circuit model consists of a resistor $R_s$ corresponding to the ohmic drop in the electrolyte, a pair $R_1$–$Q_1$, representing the elements of the double layer at the silicon/electrolyte interface, where $Q_1$ is a constant phase element, and the pair $R_2$–$Q_2$ is equivalent to the process occurring at the silicon surface during the etching process.

The values of the parameters of the electrical equivalent circuit for p-Si in different concentrations of HNO₃ are summarized in Table 1. As observed from the data in Table 1, the value of the resistance, $R_2$, decreases, while that of the constant phase element, $Q_2$, increases as the concentration of HNO₃ increases, which confirms that the rate of dissolution of the p-Si increases with increasing the HNO₃ concentration. The dissolution process leads to the formation of the porous layer.

### Table 1

<table>
<thead>
<tr>
<th>Electrolytic solution</th>
<th>$R_1$ (Ω cm²)</th>
<th>$Q_1$ (μF cm⁻²)</th>
<th>$R_2$ (kΩ cm²)</th>
<th>$Q_2$ (μF cm⁻²)</th>
<th>$R_s$ (Ω cm²)</th>
</tr>
</thead>
<tbody>
<tr>
<td>0.3 M HNO₃</td>
<td>69.12</td>
<td>1.498</td>
<td>21.06</td>
<td>1.800</td>
<td>8.394</td>
</tr>
<tr>
<td>0.5 M HNO₃</td>
<td>67.80</td>
<td>1.533</td>
<td>12.81</td>
<td>2.400</td>
<td>7.044</td>
</tr>
<tr>
<td>1.0 M HNO₃</td>
<td>37.43</td>
<td>3.800</td>
<td>8.532</td>
<td>32.017</td>
<td>4.461</td>
</tr>
<tr>
<td>2.0 M HNO₃</td>
<td>6.264</td>
<td>3.383</td>
<td>0.3186</td>
<td>45.917</td>
<td>3.706</td>
</tr>
</tbody>
</table>

3.2.2. Effect of KMnO₄ concentration

The impedance responses diagrams of p-Si etched in 3.0 M HF containing different concentrations of KMnO₄ after etching for 180 min are shown in Fig. 8. The experimental impedance data were also analyzed using the equivalent circuit shown in Fig. 7. The val-
ues of equivalent circuit elements are presented in Table 2. From these data it is clear that the value of resistance, $R_2$, increases and that of the constant phase element, $Q_2$, decreases as the concentration of KMnO$_4$ increases, indicating an increase in the thickness of the passive layer of the salt, K$_2$SiF$_6$, formed on the surface of silicon. These results are in good agreement to those obtained previously by Nahm et al. [32].

### 3.3. Effect of HF concentration

To optimize the conditions of the porous silicon formation, it was essential to investigate the effect of the concentration of the main etchant, i.e. HF. Therefore, the effect of HF concentration on the etching of p-Si in the presence of different oxidizing agents was investigated. The concentration of HF was changed from 3.0 to 11.0 M keeping the concentration of oxidizing agent constant.

#### 3.3.1. Effect of HF concentration on the etching of p-Si in 2.0 M HNO$_3$

The impedance measurements were carried out after etching of p-Si in 2.0 M HNO$_3$ solutions containing different concentrations of HF for 180 min and the results are presented in Fig. 9. The impedance data obtained were analyzed using the same equivalent circuit model shown in Fig. 7 and the parameters values of this

![Nyquist impedance plots of p-Si after etching for 180 min in 2.0 M HNO$_3$ solution containing: (a) 3.0 M HF, (b) 5.0 M HF, (c) 7.0 M HF.

#### Table 2

The effect of KMnO$_4$ concentration on the values of different fitting parameters of the equivalent circuit model assigned to p-Si in 3.0 M HF solutions.

<table>
<thead>
<tr>
<th>Electrolytic solution</th>
<th>$R_1$ (Ω cm$^2$)</th>
<th>$Q_1$ (μF cm$^{-2}$)</th>
<th>$R_2$ (kΩ cm$^2$)</th>
<th>$Q_2$ (μF cm$^{-2}$)</th>
<th>$R_s$ (Ω cm$^2$)</th>
</tr>
</thead>
<tbody>
<tr>
<td>0.1 M KMnO$_4$</td>
<td>12.3</td>
<td>5.2</td>
<td>12.7</td>
<td>164</td>
<td>25.4</td>
</tr>
<tr>
<td>0.2 M KMnO$_4$</td>
<td>25.6</td>
<td>3.0</td>
<td>41.1</td>
<td>149</td>
<td>23.9</td>
</tr>
<tr>
<td>0.3 M KMnO$_4$</td>
<td>436.3</td>
<td>0.8</td>
<td>$3.48 \times 10^3$</td>
<td>110</td>
<td>15.3</td>
</tr>
</tbody>
</table>
equivalent circuit are listed in Table 3. The resistance, $R_2$, decreases, while the value of $Q_2$ increases as the concentration of HF increases, which indicates an increase in the rate of dissolution of Si. The dissolution is accompanied by porous film formation.

3.3.2. Effect of HF concentration on the etching of p-Si in 0.2 M KMnO$_4$

The impedance experiments were carried out in the same way as described before. The impedance plots responses of p-Si in 0.2 M KMnO$_4$ solutions containing different concentrations of HF are shown in Fig. 10. The parameters values of the equivalent circuit (Fig. 7) used for the analysis of the impedance data are presented in Table 4. It is clear from the results that at low concentrations of HF, silicon dissolves with the formation of K$_2$SiF$_6$ on its surface. With increasing concentration of HF the value of $R_2$ decreases as a result of an increase in the rate of dissolution of the deposited salt. Two clear capacitive loops were recorded at 11.0 M HF indicating the presence of two time constants representing the two processes, pore formation and salt deposition onto the porous structure.

<table>
<thead>
<tr>
<th>Electrolytic solution</th>
<th>$R_1$ (Ω cm$^2$)</th>
<th>$Q_1$ (µF cm$^{-2}$)</th>
<th>$R_2$ (kΩ cm$^2$)</th>
<th>$Q_2$ (µF cm$^{-2}$)</th>
<th>$R_s$ (Ω cm$^2$)</th>
</tr>
</thead>
<tbody>
<tr>
<td>3.0 M HF</td>
<td>6.264</td>
<td>3.383</td>
<td>318.6</td>
<td>45.917</td>
<td>3.706</td>
</tr>
<tr>
<td>5.0 M HF</td>
<td>58.52</td>
<td>27.38</td>
<td>83.28</td>
<td>52.63</td>
<td>55.16</td>
</tr>
<tr>
<td>7.0 M HF</td>
<td>2.922</td>
<td>2.463</td>
<td>31.55</td>
<td>45.23</td>
<td>2.337</td>
</tr>
</tbody>
</table>

Table 3

Effect of HF concentration on the values of the fitting parameters to equivalent circuit model representing p-Si in 2.0 M HNO$_3$ solutions.
Table 4
Effect of HF concentration on the values of the fitting parameters of the equivalent circuit model representing the etching of p-Si in 0.2 M KMnO₄ solutions.

<table>
<thead>
<tr>
<th>Electrolytic solution</th>
<th>$R_1$ ($\Omega \text{cm}^2$)</th>
<th>$Q_1$ (µF cm⁻²)</th>
<th>$R_2$ (kΩ cm²)</th>
<th>$Q_2$ (µF cm⁻²)</th>
<th>$R_s$ ($\Omega \text{cm}^2$)</th>
</tr>
</thead>
<tbody>
<tr>
<td>3.0 M HF</td>
<td>25.6</td>
<td>3.0</td>
<td>41.1</td>
<td>149</td>
<td>23.9</td>
</tr>
<tr>
<td>5.0 M HF</td>
<td>223.6</td>
<td>0.03</td>
<td>15.29</td>
<td>166</td>
<td>23.9</td>
</tr>
<tr>
<td>7.0 M HF</td>
<td>17.57</td>
<td>5.31</td>
<td>0.570</td>
<td>192</td>
<td>6.558</td>
</tr>
<tr>
<td>11.0 M HF</td>
<td>179.3</td>
<td>4.517</td>
<td>0.557</td>
<td>315.7</td>
<td>18.70</td>
</tr>
</tbody>
</table>

4. Conclusions

1. By means of the impedance measurements, the formation process of stain etched porous silicon layer reveals continuous dissolution of Si surface and simultaneous formation of porous silicon at the porous/crystalline interface.

2. The thickness of the porous silicon layer increases with increasing etching time to a limiting value in the case of an etchant consisting of aqueous HF solution containing HNO₃.

3. In case of aqueous HF solution containing KMnO₄ the formation of porous silicon is followed by the deposition of the passive layer of the salt K₂SiF₆ on the pores surfaces.

4. The impedance results indicate that the thickness of K₂SiF₆ increases with increasing etching time and KMnO₄ concentration, while it decreases as the concentration of HF increases. This can be attributed to the dissolution of K₂SiF₆ in the more concentrated HF solutions.

References