

CURRICULUM VITAE

Personal Data

Name : Ahmed M. Soliman
Date of Birth : November 22nd, 1943
Place of Birth : Cairo, Egypt
Nationality : Egyptian
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Education

- Ph.D. Electrical Engineering, University of Pittsburgh, USA, April 1970.
- M.S. Electrical Engineering, University of Pittsburgh, USA, December 1967.
- B.Sc. Electrical Engineering (Honors), Cairo University, Egypt, July 1964.

Professional Experience

9/2003-Present : Professor of Electronics, Electronics and
Communications Engineering Dept., Cairo University.
Egypt
9/1997 – 9/2003 : Professor and Chairman, Electronics and
Communications Engineering Dept., Cairo University.
9/91- 9/97 : Professor and Head Microelectronics Group,
Electronics and Communications Engineering Dept.,
Cairo University.
9/90 - 8/91 : Associate Dean of Engineering and Chairman
Electrical Engineering Dept., United Arab Emirates
University.
9/87 - 8/90 : Associate Dean of Engineering and Professor
Electrical Engineering Dept., United Arab Emirates
University.
9/85 - 9/87 : Professor and Chairman Electrical Engineering Dept.,
United Arab Emirates University.
5/81- 8/85 : Professor, Electronics and Communications
Engineering Dept. , Cairo University.
6/80- 5/81 : Associate Professor, Electrical Engineering
Dept. , Florida Atlantic University, USA.
11/75 - 5/81 : Associate Professor, Electronics and

9/70 - 11/75 : Communications Engineering Dept. , Cairo University.
 Assistant Professor, Electronics and
 Communications Engineering Dept. , Cairo University.
 9/66 - 4/70 : Teaching Assistant, Electrical Engineering Dept.
 University of Pittsburgh, USA..
 9/64- 8/66 : Teaching Assistant, Electrical Engineering Dept. ,
 Cairo University.

Visiting Full Time Appointments

6/79 - 6/80 : Florida Atlantic University, USA
 9/78 - 6/79 : San Francisco State University, California, USA

Adjunct Appointments

9/81 - 6/85 : Science Department, American University
 in Cairo.
 6/80 - 3/81 : Motorola, Fort Lauderdale, Florida (Master's
 Program, FAU, Division of Continuing Education)
 3/79 - 5/79 : University of California Extension, Davis, California.
 9/78 - 12/78 : Electrical and Computer Engineering Dept.,
 University of Santa Clara, California.
 9/75 - 5/78 : American University in Cairo.

Summer Appointments

Summer 1987 : Visiting Scholar, Wien University, Austria.
 Summer 1985 : Visiting Scholar, Bochum University, Germany,
 Sponsored by DAAD.
 Summer 1970 : Research Analyst, Central Research, Rockwell
 Manufacturing Company, Pittsburgh, USA.

Summer Institutes

- Rensselaer Polytechnic Institute, Troy, New York, Nuclear Defense Design, Fallout Shelter Analysis and Environmental Engineering, Sponsored by Office of Civil Defense and the American Society of Engineering Education, Summer 1969.
- The Pennsylvania State University, University Park, Pennsylvania, "Foundations of Integrated Circuits", Sponsored by the National Institute of Science Foundation, Summer 1972.

Academic Services and Advisory Boards

- Member of the Editorial Board of the IET Proceedings Circuits, Devices and Systems till July 2017 and then promoted to Associate Editor.
- Member of the Editorial board of Analog Integrated Circuits and Signal Processing, Kluwer Academic Publishers.
- Member of the Editorial board of Electrical and Computer Engineering, Hindawi Publisher till March 2017.
- Member of the Editorial Board of the Open Signal Processing Journal.

- Member of the Editorial Board of Scientific Research and Essays, Academic Publisher
- Associate Editor IEEE Transaction on Circuits and Systems I, December 2001-December 2003.
- Associate Editor Journal of Circuits Systems and Signal Processing, January 2004-Now.
- Associate Editor of the Journal of Advanced Research (JAR) Cairo University.
- Guest Editor Special issue on: CMOS Analog Integrated Filters, Active and Passive Electronic Components 2011.
- Member of the Board of Directors of the Technology and Energy Research Center, United Arab Emirates University, Sept. 87- Aug 91.
- Member of the Advisory Panel for the Women in Science Program of the National Science Foundation, 1981.
- Member of the Advisory Committee of the Analog VLSI Workshop, Electronic Circuits, the Institute of Electrical Engineers of Japan, 1997-Now.
- Member of the Promotion Committee, Supreme Council of Universities, Arab Republic of Egypt, 1998- 2008.
- Editor IETE Journal of Education, Taylor and Francis since January 2018.

Reviewer for the Following Journals:

- IEEE Transactions on Circuits and Systems I.
- IEEE Transactions on Circuits and Systems II.
- IET Proceedings Circuits, Devices and Systems.
- Electronics Letters.
- Analog Integrated Circuits and Signal Processing.
- International Journal of Circuit Theory and Applications.
- International Journal of Electronics.
- Circuits, Systems and Signal Processing.
- Microelectronics Journal.
- Journal of Franklin Institute.
- Journal of Circuits Systems and Computers.
- Semiconductor Science and Technology.

Publications Information:

- Total number of Journal papers =383
- Total number of Conference papers presented = 110
- Total number of Book Chapters = 6
- Total number of Books = 11
- Total Impact Factor (IF) = 741.8

- Scopus Cited Documents = 456
- Scopus total Citations = 8717
- Scopus h-Index = 47

- Highest Scopus citation is 400 for a paper published in IEE in 1997.
- Google Scholar Cited Documents = 505
- Google Scholar Citations 11496
- Google Scholar h index 57
- Highest Google Scholar Citation is 561 for a paper published in IEE in 1997.

Graduate Supervision with Degrees Received:

- Supervised 10 Ph.D. Students.
- Supervised 64 Master Students.

Professional Societies

- Life Senior Member, the Institute of Electrical and Electronic Engineers, USA (SR.M. 05636659)
- Member, Institute of Electronics, Information and Communication Engineers, Japan.

Honors and Awards

- The State Engineering Science Prize from the Scientific Research Academy, Egypt: for the year 1976.
- First Class Science Medal from the Egyptian President for services to the Field of Electrical Engineering and Engineering Education, 1977.
- Included in the fifth Edition of WHO's WHO in Engineering, 1982, the American Association of Engineering Societies, Inc. NY - USA.
- Included in WHO's WHO in the World, 2000.
- Certified by the Office of Civil Defense, Washington, D.C. as a Fallout Shelter Analyst, Certificate No. 2sl-2008-69.
- Received the Electronic Engineering 1998 Design Challenge Award from Maxim Integrated Products, United Kingdom.
- Served as a Member of the Examination committee of the Ph.D. Thesis of H. Aboushady, Design for Reuse of Current-Mode Continuous-Time Sigma-Delta Analog-to-Digital Converters. University of Pierre and Marie Curie, Paris VI, France, January 2002.
- Received the Excellency Award from Center of advancement of Post Graduate Studies and Researches in Engineering Sciences, Faculty of Engineering Cairo University for the years 1997-1998 for M.S Thesis of Hassan Elwan, 2000-2001for M.S Thesis of Ali Ismail, 2002-2003 for M.S Thesis of Ahmed G Radwan , 2012-2014 for Ph.D Thesis of A. Soltan Ali and 2014-2016 for Ph.D Thesis of Lobna Said.
- Supervised E. S. Tawfik whose M.S Thesis received best Thesis Award from Cairo University 2007.
- The State Engineering Science Excellency Prize in Engineering Science from the Scientific Research Academy, Egypt for the year 2008.

- The State Engineering Science Appreciation Prize in Engineering Science from the Scientific Research Academy, Egypt for the year 2010.
- Received the First-Class Science Medal from the Egyptian President for the second time for services to Egypt 2013.
- Received Cairo University Excellency Award in Engineering for the year 2016.
- ICECS 2015 held an honoring Ceremony for Dr. Ahmed Soliman of Cairo University for his outstanding contributions for IEEE CAS society.
- Served as Honorary Chair for ICM Conference Cairo Egypt December 2016.
- Served as Honorary Chair as well as General Chair for ICM Conference Cairo Egypt December 2019.
- Recently, a report published by Stanford University on 16th October 2020, the “World’s Top 2% Scientists List” based on Scopus Database for all fields. In the "Electrical & Electronic Engineering" field, Prof. Ahmed Soliman ranked as 36 worldwide and the first Egyptian on the list.
- Top 2% in all areas from Cairo University includes 55 Professors; Prof Ahmed Soliman ranked as number 1 in the list.

Inventions

- Dr. Soliman is the inventor (with Dr. Inas Awad) of the pathological Voltage Mirror and the pathological Current Mirror.
- Dr. Soliman is the inventor (with Dr. Inas Awad) of the family of the Inverting Current Conveyors which completes the set of CCII invented by Dr Sedra and Dr Smith.

Funded Projects

- Dr. Soliman is the consultant of the STDF funded project under title Two Port Fractional-Order Oscillators and Filters Suitable for Tissue Modeling. Budget: 336800 EGP, Nile University. The duration of this project is 2018-2020.
- Dr. Soliman is the consultant of the research project: Design and Implementation of Software and Hardware Speech Encryption Techniques, the duration of this project Sept. 2016 – Sept. 2017, and funded by Cairo University.

LIST OF PUBLICATIONS

A. BOOKS

- [1] Lobna Said, Ahmed Soliman, Ahmed Madian, Active realization of doubly terminated LC ladder filters using active building blocks, ISBN: 978-3-8465-2247-9, LAP Lambert Academic Publishing GmbH & Co. KG (www.lap-publishing.com), Oct. 2011.
- [2] Ramy Saad, Ahmed Soliman, Synthesis, Analysis, and Modeling of Analog Active Circuits- Generalized Symbolic Framework: Theory and Applications, ISBN: 978-3-8484-0778-1, LAP Lambert Academic Publishing GmbH & Co. KG (www.lap-publishing.com), 2012.
- [3] A. El-kafrawy, Ahmed M Soliman, Analog CMOS VLSI Circuits: CMOS OTRA and OFC Realizations and Applications, ISBN:13: 978-3-8454-2416-3, LAP Lambert Academic Publishing GmbH & Co. KG (www.lap-publishing.com), 2011.
- [4] S. Abdelaziz, Ahmed M. Soliman, Ahmed A. Emira, Low Start Up Voltage Charge Pump for Thermoelectric Energy Harvesting, ISBN:13: 978-3-8443-8498-7, LAP Lambert Academic Publishing GmbH & Co. KG, (www.lap-publishing.com) 2012.
- [5] A. Soltan, Ahmed M Soliman, Analog Design: CMOS Realizations of the Operational Mirror Amplifier and its applications, ISBN:13:978-3-659-29368-9, LAP Lambert Academic Publishing GmbH & Co. KG, (www.lap-publishing.com) 2012.
- [6] R.F.Ahmed, Ahmed M Soliman, Ahmed G. Radwan, Testing Methods for Fault Detection in Electronic Circuits, ISBN:13:978-3-659-38363-2 LAP Lambert Academic Publishing GmbH & Co. KG, (www.lap-publishing.com) 2014.
- [7] A. Soltan, Ahmed M Soliman, Ahmed G. Radwan, Analog Circuit Design in the Fractional Domain, ISBN-13:978-3-659-60691-5 LAP Lambert Academic Publishing GmbH & Co. KG, (www.lap-publishing.com) 2014.
- [8] N.A. Khalil, Ahmed M Soliman, Utilization of Genetic Algorithms to design Active Circuits Using Nullor and Mirror, Lap Lambert Publisher, ISBN-13 :978-3-659-78304-3, LAP Lambert Academic Publishing GmbH & Co. KG, (www.lap-publishing.com) 2015.
- [9] A. El-Bayoumi, Hassan Mostafa, Ahmed M. Soliman, Design of High-Performance Differential Voltage-to-Time Converters, ISBN-13:978-3-330-01237-0, LAP Lambert Academic Publishing GmbH & Co. KG, (www.lap-publishing.com) 2016.
- [10] Osama Abdelkader, Hassan Mostafa, Ahmed M. Soliman, Low Power FPGA Design Techniques at the Device and Circuit Levels, ISBN-13: 978-620-2-01013-9, LAP Lambert Academic Publishing GmbH & Co. KG, (www.lap-publishing.com) 2017.
- [11] Maha Beheiry, Ahmed Soliman, Hassan Mostafa, Designing and Implementing Three-dimensional Network on Chips: How to create and implement different 3D-NoCs, ISBN-13: 978-620-2-07776-7, LAP Lambert Academic Publishing GmbH & Co. KG, (www.lappublishing.com) 2017.

B. BOOK Chapters

- [1] Ahmed M Soliman author of Chapter 4 in the Textbook Analog Circuits: Applications, Design and Performance, Nova Science Publisher, ISBN: 978-1-61324-355-8
Chapter 4: Transconductance Amplifiers: NAM Realizations and Applications, pp. 93-119, Nov. 2011(Feb. 2012). Editor: Esteban Tlelo-Cuautle (INAOE, Mexico)
- [2] Ahmed M Soliman author of Chapter 4 in the Textbook Integrated Circuits for Analog Signal Processing, Springer 2013, ISBN:978-1-4614-1382-0. Chapter 4: Generation of Grounded Capacitors Minimum Component Oscillators, pp. 85-105. Editor: Esteban Tlelo-Cuautle (INAOE, Mexico)
- [3] Ahmed M Soliman author of Chapter 10 in the Textbook Analog/RF and Mixed Signal Circuit Systematic Design, Springer 2013.
Chapter, Synthesis of Generalized Impedance Converter and Inverter Circuits Using NAM Expansion, pp. 223-242. ISBN: 978-3-642-36328-3
Editors: M. Fakhfakh, Esteban Tlelo-Cuautle, Rafael Castro-Lopez.
- [4] Ahmed M Soliman author of Chapter 7 in the Textbook Pathological Elements in Analog Circuit Design, Springer. Applications of the Voltage Mirror-Current Mirror in Realizing Active Building Blocks, pp.173-203. ISBN- 978-3-319-75157-3 (eBook Editors: M. Fakhfakh, M. Pierzchala, 2018.
- [5] L.A.Said, A. G. Radwan, A. H. Madian, and A. M. Soliman, Survey on Two-Port Network-Based Fractional-Order Oscillators, Fractional Order Systems Optimization, Control, Circuit Realizations and Applications Advances in Nonlinear Dynamics and Chaos: Academic Press, 2018.
- [6] N Khalil, L Said, A G Radwan, A.M. Soliman, A survey on memristor active emulation circuits in the fractional-order domain, Fractional Order Systems, An Overview of Mathematics, Design, and Applications for Engineers, Vol. 1, Chapter 9, in Emerging Methodologies and Applications in Modeling , pp. 375-410, 2022.

C. JOURNAL PAPERS

- [1] N.K. Bose and A. M. Soliman, A Novel Approach to Synthesis of Multivariable Positive Real Functions, Electronics Letters, Vol. 5, No. No 26, pp. 717-718, December 1969.
- [2] N.K. Bose and A. M. Soliman, Lossless Multi-ports with Terminations in Synthesis Problems, Electronics Letters, Vol. 6, No. 4, pp. 100-102, February 1970.
- [3] A.M. Soliman and N.K. Bose, A Decomposition Theorem for Multivariable Reactance Functions, Proceedings IEEE, Vol. 59, pp. 309-311, February 1971.
- [4] A.M. Soliman and N.K. Bose, Synthesis of a Class of Multivariable Positive Real Functions Using Bott-Duffin Technique, IEEE Transactions on Circuit Theory, Vol. CT-18, pp. 288-290, March 1971.

- [5] A.M. Soliman, A New Generation of Positive Real Functions Using the Bessel polynomials, *International Journal of Electronics*, Vol. 31, No. 4, pp. 329-332, October 1971.
- [6] A.M. Soliman, Synthesis of a Class of Two Variable Positive Real Functions, *International Journal of Electronics*, Vol. 32, No. 4, pp. 455-461, April 1972.
- [7] A.M. Soliman, Gyratorless Realization of Class of Three Variable Positive Real Functions", *International Journal of Electronics*, Vol. 32, No. 5, pp. 529-536, May 1972.
- [8] A.M. Soliman, New Generalized-Immittance Converter Circuits Obtained by Using the Current Conveyor, *International Journal of Electronics*, Vol. 32, No. 6, pp. 673-679, June 1972.
- [9] A.M. Soliman, Two New L-C Mutators and Their Realizations, *IEEE Transactions on Circuit Theory*, Vol. CT-19, pp. 371-372, July 1972.
- [10] A.M. Soliman, Active RC Realization of Current Transfer Functions Using Voltage Generalized Immittance Conveyors, *International Journal of Electronics*, Vol. 33, No. 3, pp. 273-280, September 1972.
- [11] A.M. Soliman, New Active RC Configuration for Realizing a Medium Selectivity Notch Filter, *Electronics Letters*, Vol. 8, No. 21, pp. 522-524, October 1972.
- [12] A.M. Soliman, Inductorless Realization of an All-Pass Transfer Function Using the Current Conveyor, *IEEE Transactions on Circuit Theory*, Vol. CT-20, pp. 80-81, January 1973.
- [13] A.M. Soliman, Realization of Operational-Amplifier All-Pass Networks, *Electronics Letters*, Vol. 9, No. 3, pp. 67-68, February, 1973.
- [14] A.M. Soliman, New L-R Mutators and Their Models, *International Journal of Electronics*, Vol. 34, No. 6, pp. 735-756, June 1973.
- [15] A.M. Soliman, Another Realization of an All-Pass or a Notch Filter Using a Current Conveyor, *International Journal of Electronics*, Vol. 35, No. 1, pp. 135-136, June 1973.
- [16] A.M. Soliman, A Low Sensitivity Active RC Low pass Filter *IEEE Transaction on Audio and Electro-acoustic*, Vol. AU 21, No. 4, pp. 372-374, August 1973.
- [17] A.M. Soliman, Two Active RC Configurations for Realizing Non-Minimum Phase Transfer Functions, *International Journal of Circuit Theory and Applications*, Vol. 1, No. 3, pp. 293-299, September 1973.
- [18] A.M. Soliman, A General Configuration for Realizing All-Pass or Notch Filters Using a Grounded Operational Amplifier, *International Journal of Electronics*, Vol. 35, No. 4, pp. 461-464, October 1973.
- [19] A.M. Soliman, Active RC Low-Pass Filter Suitable for Integration, *International Journal of Electronics*, Vol. 36, No. 6, pp. 799-803, June 1974.
- [20] A.M. Soliman, A New Active RC Configuration for Realizing Non-Minimum Phase Transfer Functions, *International Journal of Circuit Theory and Applications*, Vol. 2, No. 3, pp. 307-315, September 1974.
- [21] A.M. Soliman, Active RC High Selectivity Notch Filter, *International Journal of Electronics*, Vol. 37, No. 4, pp. 565-567, October 1974.

- [22] A.M. Soliman, A New Single Operational Amplifier Medium Selectivity Non-minimum Phase Network, *AEU-International Journal Electronics and Communication*, Vol. 28, No. 12, pp. 511-513, December 1974.
- [23] A.M. Soliman, Conversion of a Bandpass Resonator to an All-Pass or a Notch Filter, *International Journal of Electronics*, Vol. 38, No. 4, pp. 559-562, April 1975.
- [24] A.M. Soliman, Simple Sinusoidal Active RC Oscillators, *International Journal of Electronics*, Vol. 39, No. 4, pp. 455-458, October 1975.
- [25] F. S. Atiya, A.M. Soliman and T.N. Saadawi, Active RC Bandpass and Low Pass Filters Using the DVCCS/DVCVS, *Electronics Letters*. Vol. 12, No. 4, pp. 360-361, July 1976.
- [26] A.M. Soliman, Generalized Immittance Inverters and Their Realizations, *International Journal of Electronics* Vol. 41, No. 1, pp. 59-64, July, 1976.
- [27] A.M. Soliman, Two Novel Active RC Canonic Bandpass Networks Using the Current Conveyor, *International Journal of Electronics*, Vol. 42, No. 1, pp. 49-54, January 1977.
- [28] A.M. Soliman and S. S. Awad, Canonical High Selectivity Parallel Resonator Using a Single Operational Amplifier and Its Applications in Filters, *IEE Journal on Electronic Circuits and Systems*, Vol. 1, No. 4, pp. 145-148, July 1977.
- [29] A.M. Soliman and M. Fawzy, A Universal Active R Filter, *Electronic Engineering*, Vol. 49, No. 594, pp. 49-50, July 1977.
- [30] A.M. Soliman and M. Fawzy, A Bandpass Filter Using the Operational Amplifier Pole, *IEEE Journal of Solid State Circuits*, Vol. SC-12, No. 4, pp. 429-430, August 1977.
- [31] F.S. Atiya, A.M. Soliman and T.N. Saadawi, Active RC Nonminimum phase Network Using the DVCCS/DVCVS, *Proceedings IEEE*, Vol. 65, No. 11, pp. 1606-1607, November 1977.
- [32] A.M. Soliman, On the Generation of Multivariable Positive Real Matrices, *AEU-International Journal Electronics and Communication*, Vol. 31, No. 12, pp. 510-512, December 1977.
- [33] A.M. Soliman and S. S. Awad, A Tunable Active Inductance Using a Single Operational Amplifier and Its Applications in Filters, *AEU-International Journal Electronics and Communication*, Vol. 32, No. 1, pp. 44-48, January 1978.
- [34] A.M. Soliman and S. S. Awad, A Novel Sine Wave Generator Using a Single Operational Amplifier, *Proceedings IEEE*, Vol. 66, No. 2, pp. 253-254, February 1978.
- [35] A.M. Soliman and M. Fawzy, Active R Resonator Realization, *Proceedings IEEE*, Vol. 66, No. 2, pp. 254-255, February 1978.
- [36] A.M. Soliman, Realizations of Ideal FDNC and FDNR Elements Using New Types of Mutators, *International Journal of Electronics*, Vol. 44, No. 3, pp. 317-323, March 1978.

- [37] A.M. Soliman, Inductorless All-Pass Phase Shifter Using a Single Input Operational Amplifier, *L'onde Electrique*, Vol. 58, No. 3. pp. 238- 240, March 1978.
- [38] A.M. Soliman and M. Fawzy, A Universal Active R Biquad, *International Journal of Circuit Theory and Applications*, Vol. 6, No. 2, pp. 153-157, April 1978.
- [39] A.M. Soliman and S. S. Awad, A Canonical Voltage Controlled Oscillator Realized Using a Single Operational. Amplifier, *Frequenz*, Vol. 32, No. 5, pp. 153-154, May 1978.
- [40] A.M. Soliman and S.S. Awad, New Conversion Methods for Realizing Nonminimum Phase Transfer Functions, *Proceedings IEEE*, Vol. 66, No. 5, pp. 588-590, May 1978.
- [41] A.M. Soliman and M. Ismail, Phase Correction in Two-Integrator Loop Filters Using a Single Compensating Resistor, *Electronics Letters*, Vol. 14, No. 12, pp. 375-376, June 1978.
- [42] F. S. Atiya, A.M. Soliman and T.N. Saadawi, A Universal Second Degree Two Port Network Using a Single Operational Amplifier, *Alta Frequenza*, Vol. XLVII, No. 7, pp. 342-347, July 1978.
- [43] A.M. Soliman, Novel Variable Frequency Sinusoidal Oscillator Using a Single Current Conveyor, *Proceedings IEEE*, Vol. 66, No. 7, p. 800, July 1978.
- [44] A.M. Soliman and M. Fawzy, A New Active R Bandpass Filter, *Journal of the Franklin Institute*, Vol. 306, No. 2, pp. 159-163, August 1978.
- [45] A.M. Soliman, A Novel Inductor Simulation Using the Pole of the Operational Amplifier, *Frequenz*, Vol. 32, No. 8, pp. 239-240, August 1978.
- [46] A.M. Soliman, A Grounded Inductance Simulation Using the DVCCS/DVCVS, *Proceedings IEEE*, Vol. 66, No. 9, pp. 1089-1091, September 1978.
- [47] A.M. Soliman, Ford- Girling Equivalent Circuit Using CC II, *Electronics Letters*, Vol. 14, No. 22, pp. 721-722, October 1978.
- [48] A.M. Soliman, A New Single Operational Amplifier Active RC Bandpass Network with Reduced Sensitivity to Amplifier Gain-Bandwidth Product, *International Journal of Circuit Theory and Applications*, Vol. 6, No. 4, pp. 321-326, October 1978.
- [49] A.M. Soliman, New Active Gyrator Circuit Using a Single Current Conveyor, *Proceedings IEEE*, Vol. 66, No. 11, pp. 1580-1581, November 1978.
- [50] A.M. Soliman and M. Ismail, Op-Amp Integrators with Infinite Q- factor, *Frequenz.*, Vol. 32, No.11, pp. 331-334, November 1978.
- [51] A.M. Soliman, Realization of Frequency Dependent Negative Resistance Circuits Using Two Capacitors and a Single Current Conveyor, *Proceedings IEE*, Vol. 125, No. 12, pp. 1336-1337, December 1978.
- [52] A.M. Soliman and S. A. Badre, A Universal Notch Filter, *International Journal of Circuit Theory and Applications*, Vol. 7, No. 1, pp. 139-142 January 1979.
- [53] M. Nomair, Y. Bahnas and A.M. Soliman, Noise Relations of Inverse Active Networks and Complementary Networks, *Electronics Letters*, Vol. 15, No. 1, pp. 18-20, January 1979.

- [54] A.M. Soliman and M. Ismail, Novel Passive and Active Compensated Deboo Integrators, Proceedings IEEE, Vol. 64, No. 2, pp. 324-325, February 1979.
- [55] A.M. Soliman, A Modified Canonic Active RC Band-pass Filter with Reduced Sensitivity to Amplifier Gain Bandwidth Product, Proceedings IEEE, Vol. 67, No. 2, pp. 325-326, February 1979.
- [56] A.M. Soliman and M. Ismail, Active Compensation of Op Amps, IEEE Transactions Circuits and Systems, Vol. CAS-26, Vol. 2, pp. 112-117, February 1979.
- [57] A.M. Soliman and S.S. Awad, A Modified Sine-Wave Generator Using a Single Operational Amplifier, Electronic Engineering, Vol. 51, No. 618, p. 17, February 1979.
- [58] A.M. Soliman, A Modified Wien Bridge Oscillator, Journal of Applied Science and Engineering, The Netherlands, Vol. 3, pp. 277-279, 1979.
- [59] A.M. Soliman, A New Active C Differential Input Integrator Using the DVCCS/DVCVS, International Journal of Circuit Theory and Applications, Vol. 7, No. 2, pp. 272- 275, April 1979.
- [60] A.M. Soliman, Active Compensated Band-Pass Filter with Reduced Sensitivity to Op Amp Gain Bandwidth Product, Frequenz, Vol. 33, No. 6, pp. 149-151, May 1979.
- [61] A.M. Soliman and M. Ismail, On the Active Compensation of Non-inverting Integrators, Proceedings IEEE, Vol. 67, No. 6, pp. 961-963, June 1979.
- [62] A.M. Soliman, A Generalized Active Compensated Non-inverting VCVS with Reduced Phase Error and wide Bandwidth, Proceedings IEEE, Vol. 67, No. 6, pp. 963-965, June 1979.
- [63] A.M. Soliman and M. Ismail, A Novel Active Compensation Method of Op Amp VCVS Structures, AEU-International Journal Electronics and Communication, Vol. 33, No. 9, pp. 378-379, September 1979.
- [64] A.M. Soliman, A New Realization of the FDNC Using the DVCCS/DVCVS, AEU-International Journal Electronics and Communication, Vol. 33, No. 10, pp. 423-424, October 1979.
- [65] A.M. Soliman, Novel Generalized Differential Integrator with Controlled Phase Lead, Proceedings IEEE, Vol. 67, No. 10, pp. 1449-1451, October 1979.
- [66] A.M. Soliman, Active Phase Compensation of Op Amp VCCS Structures, Proceedings IEEE, Vol. 67, No. 10, pp. 1451-1452, October 1979.
- [67] A.M. Soliman and M. Ismail, Passive Compensation of Op-Amp VCVS and weighted Summer Building Blocks, IEEE Transactions Circuits and Systems, Vol. CAS-26, pp. 898-900, October 1979.
- [68] A.M. Soliman and M. Fawzy, Some Partially Active R Filter Circuits, Electronic and Radio Engineers, Vol. 49, No. 11, pp. 587-590, November 1979.
- [69] A.M. Soliman and M. Ismail, A Novel Passive Compensated Inverting weighted Summer, International Journal of Circuit Theory and Applications, Vol. 8, No. 1, pp. 81-85, January 1980.
- [70] A.M. Soliman, A Generalized Active R Weighted Summer, Alta Frequenza, Vol. XLIX-N. 1, pp. 35-38, January-February 1980.

- [71] A.M. Soliman, Phase Correction in Two-Integrator Loop Filters Using a New Variable Phase Inverting Amplifier, *Electronics Letters*, Vol. 16, No. 5, pp. 186-188, February 1980.
- [72] A.M. Soliman, Novel 2 OA-Three Resistor Variable Phase Inverting Amplifier and Its Application to High-Q Active Filters, *Electronics Letters*, Vol. 16, No. 8, pp. 294-295, April 1980.
- [73] A.M. Soliman, Novel Phase Lead Inverting Integrator and its Application in Two Integrator Loop Filters, *Electronics Letters*, Vol. 16 No. 12, pp. 475-476, June 1980.
- [74] A.M. Soliman, Economical wide-Band Voltage Controlled Voltage Source, *International Journal of Electronics*, Vol. 49, No. 1, pp. 77-81, July 1980.
- [75] A.M. Soliman, A Comment on Additional Types of Mutators and Active RC Synthesis Using Mutators, *International Journal of Electronics*, Vol. 49, No. 1, p. 83, July 1980.
- [76] A.M. Soliman, Comments on: Realization of an All-Pass Transfer Function Using the Second Generation Current Conveyor, *Proceedings IEEE*, Vol. 68, No. 8, p. 1035, August 1980.
- [77] A.M. Soliman, A Novel Active Phase Compensated Inverting Amplifier, *Frequenz*, Vol. 34, No. 8, pp. 238-240, August 1980.
- [78] A.M. Soliman, Passive Compensation of Inverting VCCS Structures, *Frequenz*, Vol. 34, No. 9, pp. 246-248, September 1980.
- [79] A.M. Soliman, Classification and Generation of Active Compensated Non-inverting VCVS Building Blocks, *International Journal of Circuit Theory and Application*, Vol. 8, No. 4, pp. 395-405, October 1980.
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