

Now since \hat{G} and G are related by a nonsingular congruence transformation, they have the same rank. Hence the null space of G has dimension one. It may easily be verified that the null space of G is spanned by a basis vector ϵ which has only $+1$, -1 , and 0 elements.¹ Hence

$$T\mathbf{u} = \alpha\epsilon.$$

However,

$$T = 1 + P$$

implies

$$|\alpha| = 1.$$

Thus

$$\begin{aligned} C_{TF} &= \mathbf{u}'\hat{C}\mathbf{u} = \mathbf{u}'T'CT\mathbf{u} \\ &= \epsilon' C \epsilon \leq \sum_{k=1}^n c_k \end{aligned}$$

which establishes the result.

Q.E.D.

SUMMARY

An admittance matrix G of an $n+1$ port was created by the reactance extraction method applied to an arbitrary minimal realization of an impedance which does not have a zero at infinity. G was then converted to the Foster canonic form by a linear transformation which preserved the input impedance. The result that the total capacitance was minimum for the Foster form then followed from an investigation of the properties of this transformation. The assumptions of minimality and $z(s)$ not having a zero at infinity were utilized to insure the existence of G . It is clear, however, that the result can be immediately extended to any minimal realization in which the pole at infinity of the admittance is realized by a single capacitor in shunt with an impedance which does not have a zero at infinity. Since the Foster realization contains a shunt capacitor of exactly the same value, the inequality remains the same. Furthermore, it is clear that the result extends to many RC active and passive transfer function cases whose realization is reduced to that of a driving-point impedance.

It is certainly of interest to consider generalizations of the result to realizations which are not minimal [1]. There are two situations which complicate this present procedure. First, G may not exist. Secondly, even if G exists, the rank of \hat{G} is arbitrary. Thus the null space of G contains any vector formed by a linear combination of ϵ vectors. Hence the conclusion that $T\mathbf{u}$ is an ϵ vector is no longer valid.

Finally, it should be noted that in many applications it may be more appropriate to minimize a linear combination of the total resistance and total capacitance. The case of minimizing the product of the total resistance and the total capacitance has been investigated for RC distributed networks by Protonotarios and Wing [6].

APPENDIX

CHARACTERIZATION OF THE NULL SPACE OF THE ADMITTANCE MATRIX OF AN ARBITRARY TRANSFORMERLESS RESISTOR n -PORT

It has been shown that the admittance matrix of a transformerless resistor n -port exists if and only if the ports do not form any closed paths [2], [3]. Furthermore, it may be assumed without loss of generality that the n -port forms a connected network.² In this case the admittance matrix G is given by [2], [3].

$$G = B'_{11}(B_2 D_r B'_{12})^{-1} B_{11}.$$

Here $[B_{11} B_{12}]$ is a fundamental circuit matrix of the graph containing the resistor edges as well as edges corresponding to the ports (often called the augmented graph); the submatrix B_{11} corresponds to all the edges which are ports, and D_r is the diagonal edge resistance matrix. The validity of the

¹ This result is well known for the case where G is a nodal admittance matrix. To the author's knowledge, the result is not documented for the general n -port case. A proof is, therefore, included in the Appendix.

² If this is not the case then the admittance matrix is a direct sum of the admittance matrices of each component and the necessary modifications are obvious.

formula may be established by showing that B_{12} has full rank, cf. [3]. This implies that $(B_{12} D_r B'_{12})^{-1}$ is a positive definite matrix. The proof now proceeds as follows.

The positive definiteness of $(B_{12} D_r B'_{12})^{-1}$ and G implies that the null spaces of G and B_{11} are identical. However, B_{11} is totally unimodular,³ and the basis vectors of the null space of B_{11} may be taken to have only $+1$, -1 , and 0 elements [5].

It should be noted that this surprisingly simple result has probably been overlooked because the popular method of obtaining G is not by means of this simple formula but by augmentation and pivotal condensation, cf. [2] and [3] for a complete discussion.

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F. T. BOESCH
Bell Telephone Labs., Inc.
Whippany, N. J. 07981
J. D. HAGOPIAN
Dep. Elec. Eng. Comput. Sci.
Electron. Res. Lab.
Univ. California
Berkeley, Calif. 94720

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³ A matrix is said to be totally unimodular if all of its submatrices have determinant $+1$, -1 , or 0 .

Synthesis of a Class of Multivariable Positive Real Functions Using Bott-Duffin Technique

Abstract—The applicability of the classical Bott-Duffin synthesis technique to a class of multivariable positive real functions is discussed, and the results arrived at are illustrated by an example.

I. INTRODUCTION

The synthesis of rational positive real functions of the complex frequency variable s by the Bott-Duffin procedure is well established in classical realizability theory [1]. The concept of positive real functions of several variables was introduced in 1960 by Ozaki and Kasami [2]. Since then, a considerable amount of literature has appeared on the subject. Very recently, Koga [3] solved the general problem of synthesizing an arbitrarily prescribed $(n \times n)$ positive real matrix of several variables. The classical importance of the Bott-Duffin procedure and the role of positive real functions of several variables in modern network theory motivates the writing of this correspondence. Here, it is shown that a class of multivariable positive real functions can be synthesized by the Bott-Duffin procedure.

II. MULTIVARIABLE BOTT-DUFFIN SYNTHESIS

This correspondence is based on a generalization of Saito's extension of the well-known Richards' theorem for a single variable [4]. This generalized version of Saito's result [5] is written in the form of Theorem 1.

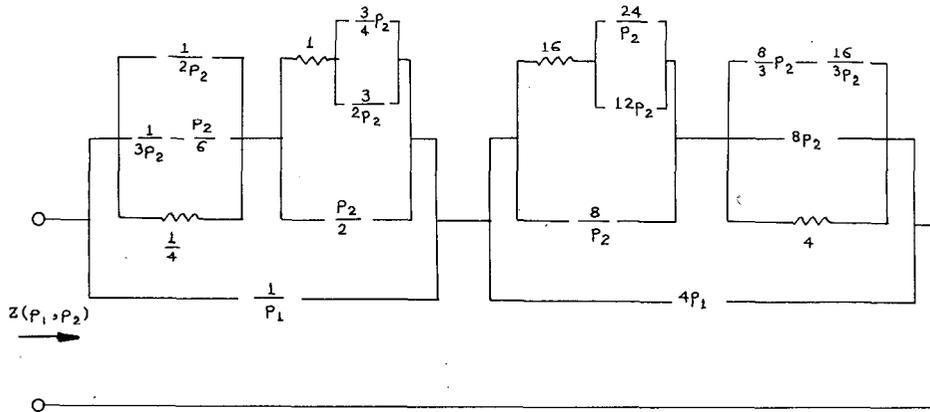


Fig. 1.

Example

Consider the realization of the following two-variable driving-point impedance function

$$Z(p_1, p_2) = \frac{4p_1p_2^2 + 4p_1p_2 + p_2^2 + p_2 + 16p_1 + 1}{p_1p_2^2 + p_1p_2 + p_2^2 + p_2 + p_1 + 4}$$

It can be seen that

$$Z(\frac{1}{2}, p_2) = -Z(-\frac{1}{2}, p_2) = 2$$

and

$$\deg_{p_1} Z(p_1, p_2) = 1$$

which thus satisfy the conditions of Theorem 2. $Z(p_1, p_2)$ can be decomposed as

$$Z(p_1, p_2) = \frac{1}{\frac{p_2^2 + p_2 + 4}{p_2^2 + p_2 + 1} + p_1} + \frac{1}{\frac{p_2^2 + p_2 + 1}{4(p_2^2 + p_2 + 4)} + \frac{1}{4p_1}}$$

whence

$$f_1(p_2) = \frac{p_2^2 + p_2 + 1}{p_2^2 + p_2 + 4}; \quad f_2(p_2) = \frac{4(p_2^2 + p_2 + 4)}{p_2^2 + p_2 + 1}$$

Using the classical Bott-Duffin procedure, $f_1(p_2)$ and $f_2(p_2)$ can be realized. The complete realization of $Z(p_1, p_2)$ is shown in Fig. 1.

III. CONCLUSIONS

It has been shown that if multivariable positive real functions, which are in general difficult to realize, satisfy some conditions, then the classical Bott-Duffin procedure can be utilized to yield network realization of those multivariable positive real function of degree one in all but one variable. It is noted that Theorem 3 is more general than Theorem 2. However, the conditions in Theorem 2 might be easier to test for in many cases. Theorem 4 makes feasible realization of higher degree functions by the Bott-Duffin technique. It is seen, however, that unlike in the single variable case, the scope of multivariable positive real function synthesis using the Bott-Duffin technique is limited.

A. M. SOLIMAN¹
N. K. BOSE
Dep. Elec. Eng.
Univ. Pittsburgh
Pittsburgh, Pa. 15213

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Minimal Realizations of Nonminimum Phase Biquadratic RC Voltage Transfer Functions

Abstract—Complete solution is given to the problem of realizing, with the minimum possible number of elements, a biquadratic nonminimum phase RC voltage transfer function, having zeros anywhere in the s plane. All possible equivalent realizations are discussed, and it is shown that there exists a great flexibility in choosing the network elements.

I. INTRODUCTION AND SCOPE OF THE PAPER

In a recent communication, Advany and Reddy [1] presented balanced realizations of nonminimum phase RC biquadratic voltage transfer functions. These realizations are minimal except in the case of one zero on the negative real axis and the other on the positive real axis. For this particular case, Advani and Reddy were unable to obtain simple networks like those possible in the other cases and they followed the method of Lin and Siskind [2] with slight modification to get a seven-element realization.

The approach adopted in [1] is to guess the network realization, calculate its transfer function, and obtain element values by comparing this with the specified one. In this correspondence, we use a network synthetic approach to the problem and derive minimal realizations for all cases of zero configurations, including the one just mentioned. For each case of zero configuration, all possible equivalent realizations are discussed. Our approach also leads to a great flexibility in the choice of element values for the synthesized networks.

¹ Now with the College of Steubenville, Steubenville, Ohio 43952.